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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	
	Filing Date	
	First Named Inventor	Chambers et al.
	Group Art Unit	
	Examiner Name	

Sheet	1	of	1	Attorney Docket No.	TI-36031

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
✓	AA	5,804,848		Mukai	09/08/1998	Entire Document
✓	AB	5,899,710		Mukai	05/04/1999	Entire Document
✓	AC	2003/0136963	A1	Krivokapic et al.	07/24/2003	Entire Document
	AD					
	AE					

FOREIGN PATENT DOCUMENTS

Exam. Initials*	Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
	BA							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
✓	CA	"25nm CMOS Omega FETs", IEEE, FU-LIANG YANG, HAO-YU CHEN, FANG-CHENG CHEN, CHANG-CHUAN HUANG, CHANG-YUN CHANG, HSIEN-KRANG CHIU, CHI-CHUANG LEE, CHI-CHUN CHEN, HUAN-TSUANG HUANG, CHIH-JIAN CHEN HUN-JAN TAO, YEE-CHIA YEO, MONG-SONG LIANG and CHENMING HU, 2002, 4 pgs.	
	CB	"SOI Devices for Sub-0.1 µm Gate Lengths", PROC. 23rd International Conference on Microelectronics (Miel 2002), Vol 1, NIS, Yugoslavia, May 12-15, 2002, J.P. COLINGE, J.T. PARK and C.A. COLINGE, pp. 109-113.	
	CC	"Advanced Depleted-Substrate Transistors: Single-Gate, Double-Gate and Tri-Gate", 2002 International Conference on Solid State Devices and Materials (SSDM 2002, Nagoya, Japan, ROBERT CHAU, BRIAN DOYLE, JACK KAVALIEROS, DOUGLAS BARLAGE, ANAND MURTHY, MARK DOCZY, RAFAIL RIOS, TOM LINTON, REZA ARGHAVANI, BEN JIN, SUMAN DATTA and SCOTT HARELAND, pp.1-23.	
✓	CD	"Beyond the Conventional Transistor", IBM J. Res. & Diev., Vol. 46, No. 2/3, March/May 2002, H.-S.P. WONG, PP. 133-168.	
	CE		
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	CG		
	CH		

Examiner Signature		Date Considered	3/2005

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